



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: El-Kareh et al. Art Unit: TBD
Serial No.: 10/822,078 Examiner: TBD
Filing Date: 04/08/2004 Docket No.: TID-34937
Customer No.: 23494 Conf. No.: TBD
Title: METHOD OF FABRICATING COMPLEMENTARY BIPOLAR TRANSISTORS
WITH SiGe BASE REGIONS

TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Attn: Official Draftsperson

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)
I hereby certify that the above correspondence is being
deposited with the U.S. Postal Service as First Class Mail
in an envelope addressed to: Commissioner of Patents,
P. O. Box 1450, Alexandria, VA 22313-1450.

Date

Marianna Smith
Marianna Smith

Submitted herewith are two (2) sheets of formal drawings.

Charge any necessary fee to Deposit Account No. 20-0668. This authorization is
enclosed in triplicate.

Respectfully submitted,

Jacqueline J. Garner
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